

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	131	control with match with label	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/10 12:24
L2	10	control with match with label same final\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/10 12:24
L3	10	control with match with label same final\$7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/10 12:24
L7	48	"expression temporary"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/10 13:33
L8	371	"temporary objects"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/10 13:35
L9	2	"temporary objects" with exception	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/10 13:35
S1	246	(717/146).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/08 10:26
S2	55	exception and S1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 15:55

S3	28	((("5742828") or ("5768595") or ("5857105") or ("5943499") or ("5937195") or ("5999739") or ("6009273") or ("6070011") or ("6148302") or ("6182284") or ("6249910") or ("6353924") or ("6374368") or ("6460178") or ("6481008") or ("6625808") or ("6662356") or ("6678805") or ("6745383") or ("6748584") or ("20020083425") or ("20030101380") or ("20020166115") or ("20030101335") or ("20030217197") or ("20030217196") or ("20030226133") or ("20020170044")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/07/07 16:10
S4	6	exception and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 16:10
S5	1119	"intermediate representation"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/10 09:44
S6	978	(717/114,116,118,148).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/08 10:28
S7	62	S5 and S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 10:29
S8	4316	exception adj handl\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 13:46
S9	96	S8 and S5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 10:29
S10	11	S6 and S9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 11:18

S11	4	(exception adj handl\$3) with (intermediate adj representation)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 13:49
S12	6	(exception adj handl\$3) same (intermediate adj representation)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 13:49
S13	2	S12 not S11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 13:49
S14	1119	"intermediate representation"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 13:51
S15	978	(717/114,116,118,148).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/08 13:51
S16	62	S14 and S15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 15:18
S17	0	hir same exception	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 15:18
S18	30	hir same code	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:02
S19	1971	final\$7 with ((intermediate adj representation) IR)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:03
S20	978	(717/114,116,118,148).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/08 18:03


S21	5	S19 and S20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:05
S22	176	(exception adj handl\$3) with final\$7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:05
S23	9	S20 and S22	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:16
S24	11	transfer with accept\$4 with code with block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:18
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S26	17	S25 not S24	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:26
S27	3	finalization with control with block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:36
S28	1030	accept with control with block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:28
S29	1	S20 and S28	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:29
S30	201	accept near3 control near3 block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:29

S31	11	accept near3 control near3 block with (instruction statement)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:32
S32	1	final same finally same endfinally	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:32
S33	2	finalization with block same exception	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:36
S34	2	finalization same block same exception	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:36
S35	25	finalization same exception	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:39
S36	7	finalization near block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:40
S37	3693	finally near block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 18:41
S38	10	S20 and S37	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 19:19
S39	191	initializ\$5 near code with block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 19:19
S40	4	S20 and S39	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 19:30

S41	1	"expression temporary object"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 19:40
S42	5	finalization with destructor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/10 08:05
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S50	833	(714/48).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 09:44

S51	126	exception and S50	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/10 09:44
S52	0	S43 and S50	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/10 09:44
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S59	19	true same false same condition same instruction same label	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/10 10:09
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1 [Open runtime platform: flexibility with performance using interfaces](#)

Michal Cierniak, Brian T. Lewis, James M. Stichnoth

November 2002 **Proceedings of the 2002 joint ACM-ISCOPE conference on Java Grande**Full text available: [pdf\(300.99 KB\)](#)
 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

According to conventional wisdom, interfaces provide flexibility at the cost of performance. Most high-performance Java virtual machines today tightly integrate their core virtual machines with their just-in-time compilers and garbage collectors to get the best performance. The Open Runtime Platform (ORP) is unusual in that it reconciles high performance with the extensive use of well-defined interfaces between its components. ORP was developed to support experiments in dynamic compilation, garb ...

Keywords: JVM, Java, dynamic compilation, garbage collection, interface design, interfaces, just-in-time compilation, modular components, virtual machine

2 [LLVM: A Compilation Framework for Lifelong Program Analysis & Transformation](#)

Chris Lattner, Vikram Adve

March 2004 **Proceedings of the international symposium on Code generation and optimization: feedback-directed and runtime optimization**Full text available: [pdf\(256.90 KB\)](#)
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This paper describes LLVM (Low Level Virtual Machine), a compiler framework designed to support transparent, lifelong program analysis and transformation for arbitrary programs, by providing high-level information to compiler transformations at compile-time, link-time, run-time, and in idle time between runs. LLVM defines a common, low-level code representation in Static Single Assignment (SSA) form, with several novel features: a simple, language-independent type-system that exposes the primitives common ...

3 [Practicing JUDO: Java under dynamic optimizations](#)

Michał Cierniak, Guei-Yuan Lueh, James M. Stichnoth

May 2000 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 2000 conference on Programming language design and implementation**, Volume 35 Issue 5Full text available: [pdf\(190.06 KB\)](#)
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A high-performance implementation of a Java Virtual Machine (JVM) consists of efficient implementation of Just-In-Time (JIT) compilation, exception handling, synchronization mechanism, and garbage collection (GC). These components are tightly coupled to achieve



self intermediate representation

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MP Plezbert, RK Cytron - Proceedings of the 24th ACM SIGPLAN-SIGACT symposium on ..., 1997 - portal.acm.org
 ... Portland, OR. 6 Urs Hölzle, Adaptive optimization for **self**: reconciling high performance with exploratory programming, Stanford University, Stanford, CA, 1995. ...
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A framework for learner modelling

P Dillenbourg, J **Self** - Interactive Learning Environments, 1992 - ile2005-t3.tripod.com
 ... Pierre Dillenbourg and John **Self** ... 7 **representation** fits with the system's **representation**. ...
 Some ambiguity arises because some **intermediate** steps of the inference ...
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[book] Eiffel: the language

B Meyer - Prentice-Hall Object-Oriented Series, 1992 - Prentice-Hall, Inc. Upper Saddle River, NJ, USA
 Cited by 709 - [Web Search](#) - [Library Search](#)

Similarity renormalization, Hamiltonian flow equations, and Dyson's **intermediate representation**

TS Walhout... - Physical Review D, 1999 - link.aps.org
 ... inV 0 will be cancelled by divergences in the **self-energy** cor ... rely on such prior work in determining the dressed Hamiltonian in the **intermediate representation**. ...
 Cited by 17 - [Web Search](#) - arxiv.org - arxiv.org - adsabs.harvard.edu - all 5 versions »

Clarity MCode: A Retargetable **Intermediate Representation** for Compilation

BT Lewis, LP Deutsch, TC Goldstein - **Intermediate** Representations Workshop, 1995 - portal.acm.org
 ... MCode implementation already has information about program types, but it could benefit from some **Self** ideas for ... 3.1 The MCode **intermediate representation** ...
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Self-Similar Solutions as **Intermediate Asymptotics**

G Barenblatt, YB Zel'dovich - Annual Review of Fluid Mechanics, 1972 - arjournals.annualreviews.org
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Pegasus: an efficient **intermediate representation**

M Budiu, SC Goldstein - reports-archive.adm.cs.cmu.edu
 ... 8 Page 11. Pegasus is **self-contained**. ... Pegasus is the **intermediate representation** of the CASH compiler, which translates C programs into hardware circuits. ...
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E Amir, S McCanne, H Zhang - ACM Multimedia, 1995 - portal.acm.org
 ... across the gateway --- they must be modified in a **self-consistent** fashion ... by a module that decodes the incoming bit stream into an **intermediate representation**. ...
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Shape blending using the star-skeleton **representation**

M Shapira, A Rappoport - IEEE Computer Graphics and Applications, 1995 - cs.huji.ac.il
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1 [A study of exception handling and its dynamic optimization in Java](#)

Takeshi Ogasawara, Hideaki Komatsu, Toshio Nakatani

 October 2001 **ACM SIGPLAN Notices , Proceedings of the 16th ACM SIGPLAN conference on Object oriented programming, systems, languages, and applications**, Volume 36 Issue 11

Full text available: pdf(190.18 KB)

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Optimizing exception handling is critical for programs that frequently throw exceptions. We observed that there are many such exception-intensive programs in various categories of Java programs. There are two commonly used exception handling techniques, stack unwinding optimizes the normal path, while stack cutting optimizes the exception handling path. However, there has been no single exception handling technique to optimize both paths.

2 [Practicing JUDO: Java under dynamic optimizations](#)

Michał Cierniak, Guei-Yuan Lueh, James M. Stichnoth

 May 2000 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 2000 conference on Programming language design and implementation**, Volume 35 Issue 5

Full text available: pdf(190.06 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A high-performance implementation of a Java Virtual Machine (JVM) consists of efficient implementation of Just-In-Time (JIT) compilation, exception handling, synchronization mechanism, and garbage collection (GC). These components are tightly coupled to achieve high performance. In this paper, we present some static and dynamic techniques implemented in the JIT compilation and exception handling of the Microprocessor Research Lab Virtual Machine (MRL VM), ...

3 [EPIC compilation: Optimization for the Intel® Itanium® architecture register stack](#)

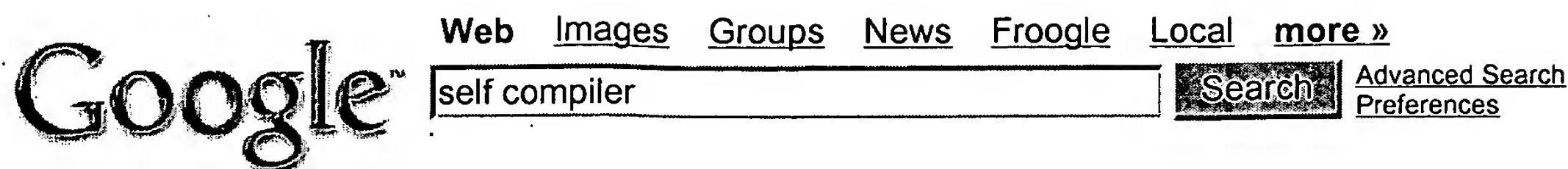
Alex Settle, Daniel A. Connors, Gerolf Hoflehner, Dan Lavery

 March 2003 **Proceedings of the international symposium on Code generation and optimization: feedback-directed and runtime optimization CGO '03**

Full text available: pdf(906.60 KB)

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The Intel® Itanium® architecture contains a number of innovative compiler-controllable features designed to exploit instruction level parallelism. New code generation and optimization techniques are critical to the application of these features to improve processor performance. For instance, the Itanium® architecture provides a compiler-controllable virtual register stack to reduce the penalty of memory accesses associated with procedure

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How do you set **self.compiler.include_dirs** from configure? Joe Nall joe@nall.com 27

Mar 2003 21:20:53 -0800. Previous message: Proposed standard python ...

mail.python.org/pipermail/ python-list/2003-March/155532.html - 3k - [Cached](#) - [Similar pages](#)

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try-except Statement (C/C++ Language Reference)

The **try-except** statement is a Microsoft extension to the C and C++ languages that enables 32-bit target applications to gain control when events that ...

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ActivePython 2.4 - Online Docs : PEP 341 -- Unifying **try-except** ...

There are many use cases for the **try-except** statement and for the try-finally statement per se; however, often one needs to catch exceptions and execute ...

aspn.activestate.com/ASPN/docs/ActivePython/2.4/peps/pep-0341.html - 115k - [Cached](#) - [Similar pages](#)

Re: [Tutor] Cannot fix OSError (**try/except**) :: ASPN Mail Archive ...

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aspn.activestate.com/ASPN/Mail/Message/python-Tutor/1986874 - 20k - [Cached](#) - [Similar pages](#)

compiler.ast.TryExcept

Package compiler :: Module ast :: Class **TryExcept**. [show private | hide private].

[frames | no frames]. Class **TryExcept**. Node --+ | **TryExcept** ...

epydoc.sourceforge.net/stdlib/public/compiler.ast.TryExcept-class.html - 9k - [Cached](#) - [Similar pages](#)

[fpc-pascal]'try-except'-problem

[fpc-pascal]'try-except'-problem. Dominik Schulz dschulz.!.@.!. c-nit.de Sat, 16 Sep 2000 00:24:09 +0200 ...

www.nl.freepascal.org/lists/fpc-pascal/2000-September/000068.html - 4k - Jul 8, 2005 - [Cached](#) - [Similar pages](#)

[fpc-pascal]TRY EXCEPT Bug

[fpc-pascal]TRY EXCEPT Bug. Pierre Muller pierre.!.@.!. idfix.wisa.be Sun, 01

Oct 2000 23:54:56 +0200. Previous message: [fpc-pascal]DOS memory access via ...

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[Psycopg] **try/except** aborted transaction error

I think what I should do is copy_from and **try/except**. -- albert chin (china at thewrittenword.com). Previous message: [Psycopg] **try/except** aborted ...

lists.initd.org/pipermail/psycopg/2003-October/002339.html - 6k - [Cached](#) - [Similar pages](#)

[Zope-dev] 2.2.0b3 - **try/except** changed?

[Zope-dev] 2.2.0b3 - **try/except** changed? Dr. Ross Lazarus rossl@med.usyd.edu.au Tue, 27 Jun 2000 12:58:24 +1000. Previous message: [Zope-dev] calling ...